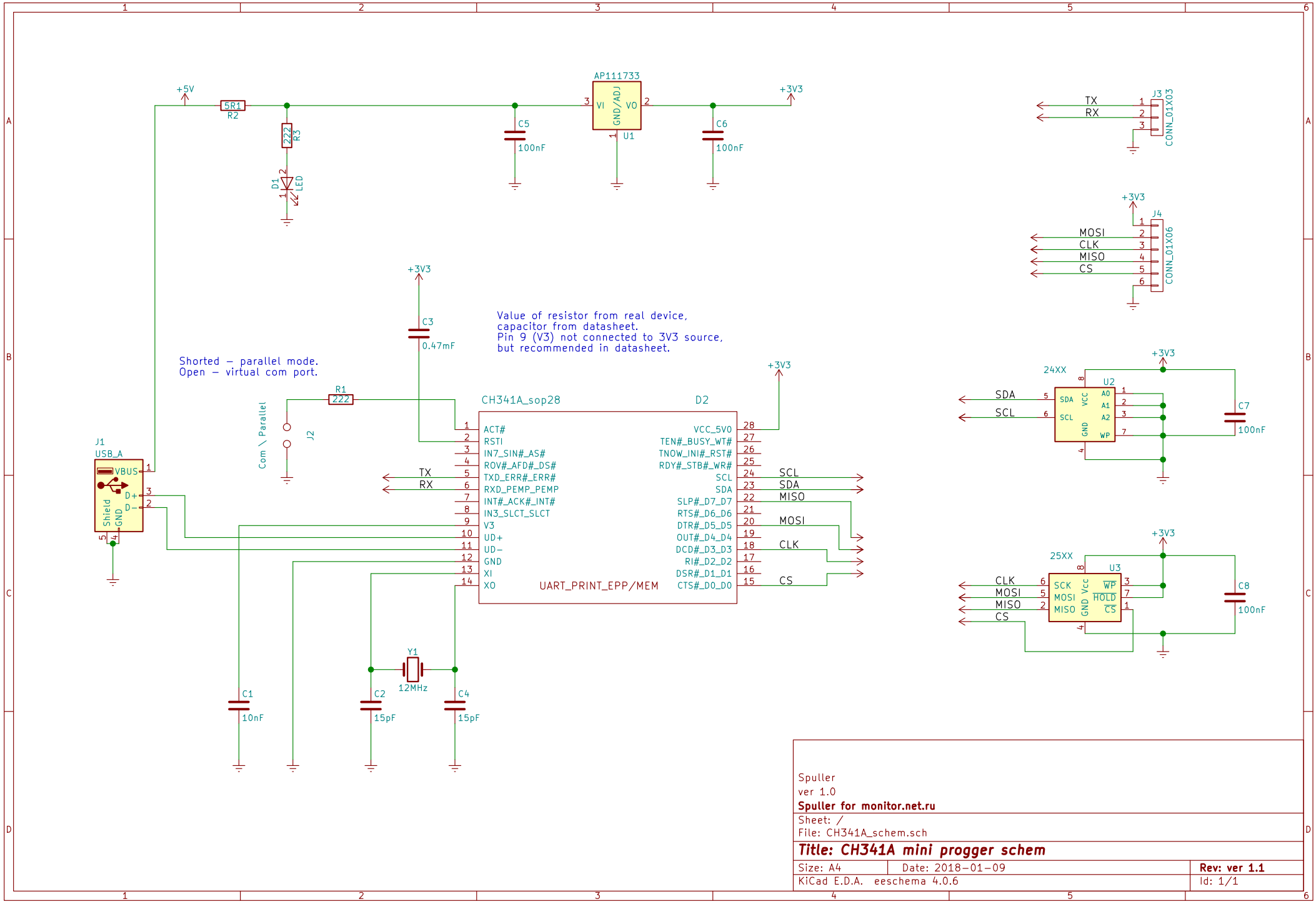
+5V
5R1
R2
222
R3
D1
LED
AP111733
VI
VO
GND/ADJ
U1
C5
100nF
C6
100nF
+3V3
TX
RX
J3
CONN_01X03
MOSI
CLK
MISO
CS
J4
CONN_01X06
C3
0.47mF
Value of resistor from real device,
capacitor from datasheet.
Pin 9 (V3) not connected to 3V3 source,
but recommended in datasheet.
Shorted – parallel mode.
Open – virtual com port.
R1
222
Com \ Parallel
J2
J1
USB_A
VBUS
D+
D–
Shield
GND
CH341A_sop28
D2
ACT#
RSTI
IN7_SIN#_AS#
ROV#_AFD#_DS#
TXD_ERR#_ERR#
RXD_PEMP_PEMP
INT#_ACK#_INT#
IN3_SLCT_SLCT
V3
UD+
UD–
GND
XI
XO
VCC_5V0
TEN#_BUSY_WT#
TNOW_INI#_RST#
RDY#_STB#_WR#
SCL
SDA
SLP#_D7_D7
RTS#_D6_D6
DTR#_D5_D5
OUT#_D4_D4
DCD#_D3_D3
RI#_D2_D2
DSR#_D1_D1
CTS#_D0_D0
UART_PRINT_EPP/MEM
24XX
U2
SDA
SCL
VCC
GND
A0
A1
A2
WP
C7
100nF
25XX
U3
SCK
MOSI
MISO
Vcc
GND
WP
HOLD
CS
C8
100nF
Y1
12MHz
C1
10nF
C2
15pF
C4
15pF
Spuller
ver 1.0
Spuller for monitor.net.ru
Sheet: /
File: CH341A_schem.sch
Title: CH341A mini progger schem
Size: A4
Date: 2018-01-09
Rev: ver 1.1
KiCad E.D.A. eeschema 4.0.6
Id: 1/1